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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP02/13555 12/25/2002

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-397575 12/27/2001

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				

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## TITLE

Semiconductor storage device and refresh control method thereof

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